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A: You may just want to replace the offending line from your code: urllib.parse.quote(repo_id) with urllib.quote(repo_id, safe='') You may also want to look at the format of your repo_id: Possibly with something like the following: id = form.save(commit=False) id.repo_id = repo_id id.save() 1. Field of the Invention This invention relates to the field of microprocessor devices, and more specifically to microprocessor pipeline structures. 2. Description of the Relevant Art Early microprocessors utilized a pipeline architecture. The pipeline architecture typically had multiple pipeline stages each comprising a variety of sub-stages that processed an operation on a data bit, byte, word, or other appropriate data element. Typically, the performance of the microprocessor was limited by the pipeline stages. The number of stages was fixed, and consequently, the microprocessor could not perform operations at a very high rate. Modern microprocessors have many pipeline stages, and consequently, can operate at a much higher rate. Not only are modern microprocessors capable of operating at a higher rate, they are usually required to operate at a higher rate. Consequently, the clock rate at which a microprocessor operates has become increasingly important. With each increase in the clock rate, many more cycles of the clock are processed by the pipeline stages. It is therefore important to maximize the average cycle time, or average throughput, of the pipeline. The pipeline stages must be able to complete a cycle during each clock cycle. The pipeline stages must also finish the pipeline during each clock cycle. Pipeline stages that are not finished when the clock transitions to the next cycle are stalled. Stalled pipeline stages waste clock cycles and do not contribute to pipeline throughput. One common source of pipeline stall is a stage that is

ready to process the next instruction, but is in fact stalled by an earlier branch instruction. The later branch instruction may cause the processor to speculate and determine that the prediction was not correct. Stalling occurs during such speculation because the earlier branch instruction was unable to make its decision and commit to the proper execution path. One solution to this problem is to prevent the branch instruction from committing to the correct execution path until after the branch instruction has processed its input. Another solution is to provide the pipeline structure with two paths to the branch execution path. Unfortunately f678ea9f9e

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